	Reg. No:			
SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR				
(AUTONOMOUS)				
	B. Tech TV Year T Semester Regular Examinations February-2022 NEURAL NETWORKS AND FUZZY LOGIC			
	(Electrical and Electronics Engineering)			
	Time: 3 hours Max.	Marks: 60		
$\frac{PART-A}{2}$				
1	(Answer all the Questions $5 \times 2 = 10$ Marks)	т 1	3 1	
I	 a Mention the basic parts of Biological Neuron. b Generally which activation function is used in input layer of the ANN 	LI T 1	2NI 2M	
	c Define associative memory	L1	21VI 2M	
	d Define fuzzy set.	L1	2M	
	e Give three defuzzyfication methods.	L1	2M	
	PART-B			
(Answer all Five Units 5 x $10 = 50$ Marks)				
	UNIT-I			
2	a Explain organization of human brain	L1	5M	
	b Discuss the functioning of biological neuron.	L2	5M	
2	OR	T 2	7) 4	
3	 a How artificial neuron is inspired from the biological neuron? Explain. b Explain the basic architecture of McCulloch Pitts neuron model and realize 3-input. 	L2 I 3	JIVI 5M	
	NAND gate using McCulloch – Pitts model	LJ	3111	
	UNIT-II			
4	Explain supervised learning in detail with block diagram.	L2	10M	
	OR			
5	Give the perceptron weight-updating rule and the learning algorithm.	L3	10M	
	UNIT-III			
6	What is associative memory? Explain briefly.	L2	10M	
	OR			
7	Briefly explain the working principle of hetero correlators.	L2	10M	
	UNIT-IV			
8	Compare and contrast Fuzzy vs Crisp.	L2	10M	
0	OR Consider set $\mathbf{V} = \{2, 4, 6, 8, 10\}$ then find its new ar set condinality and condinality of	т 2	5M	
7	a consider set $X = \{2, 4, 0, 0, 10\}$ then this power set, calculating and calculating of nower set	LJ	3111	
	b Explain the operations and properties over a fuzzy relation.	L2	5M	
		_		
10	Explain the process of fuzzification in fuzzy logic.	L3	10M	
-	OR	_		
11	Explain fuzzy inference using Modus ponens and Modus tollens.	L2	10M	

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END